

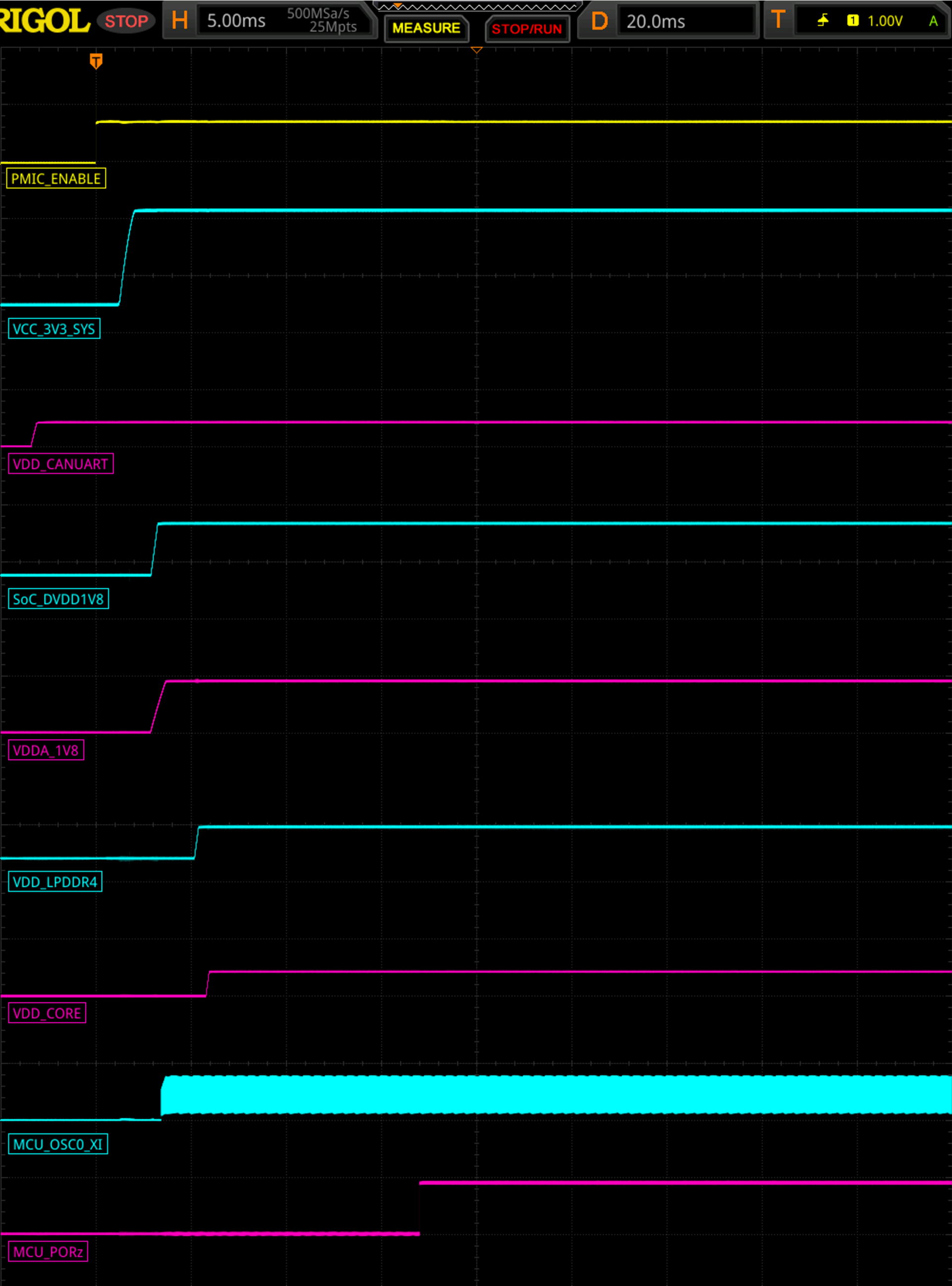


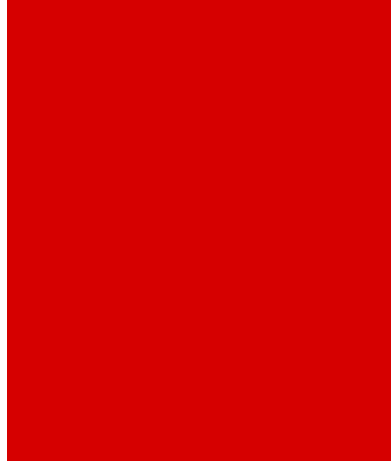
**SK-AM62P-LP SKEVM**  
**REV E1P1**  
**PROC164E1.1**  
**Power up Sequence**

# Power Up sequence timing measurements



Power Supply	Voltage (V)	Elapsed Time (mS)
PMIC ENABLE	> 1.32	0
SoC_DVDD3V3	3.3	1.25
SoC_DVDD1V8	1.8	2.95
VDDA_1V8	1.8	2.95
VDD_LPDDR4	1.1	5.2
VDD_CORE	0.85	5.75
VDDR_CORE (Sourced from VDD_CORE)	0.85	5.75
VDD_CANUART (Always On)	0.85	-3.35
MCU_OSC0_XI	1.8	3.45
MCU_PORz (Reset)	1.8	17





**SK-AM62P-LP SKEVM**  
**REV E1P1**  
**PROC164E1.1**  
**Power Down Sequence**

# Power Down sequence timing measurements

Power Supply	Voltage (V)	Elapsed Time (mS)
PMIC ENABLE	1.32	0
SoC_DVDD3V3	3.3	3.5
SoC_DVDD1V8	1.8	2.35
VDDA_1V8	1.8	2.35
VDD_LPDDR4	1.1	1.85
VDD_CORE	0.85	1.35
VDDR_CORE (Sourced from VDD_CORE)	0.85	1.35
VDD_CANUART (Always On)	0.85	105
MCU_OSC0_XI	1.8	2.35
MCU_PORz (Reset)	1.8	0.35

